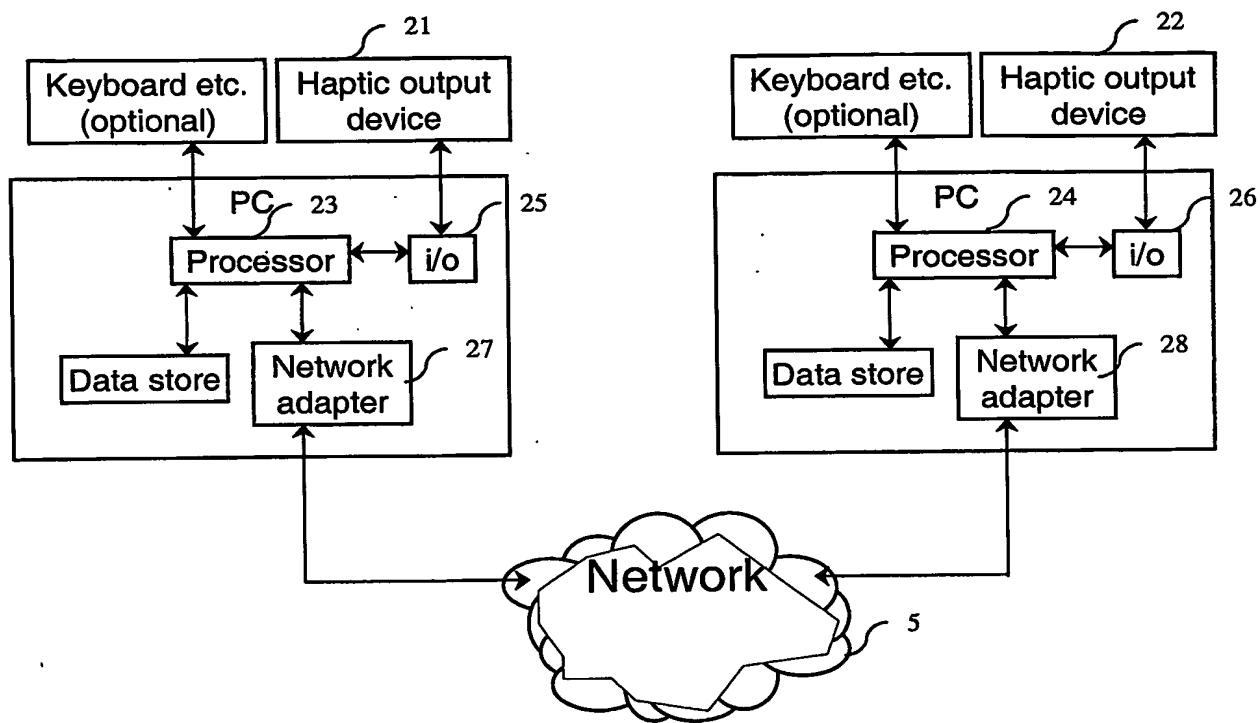


Fig 1

Hardware

2/8

**Fig 2**

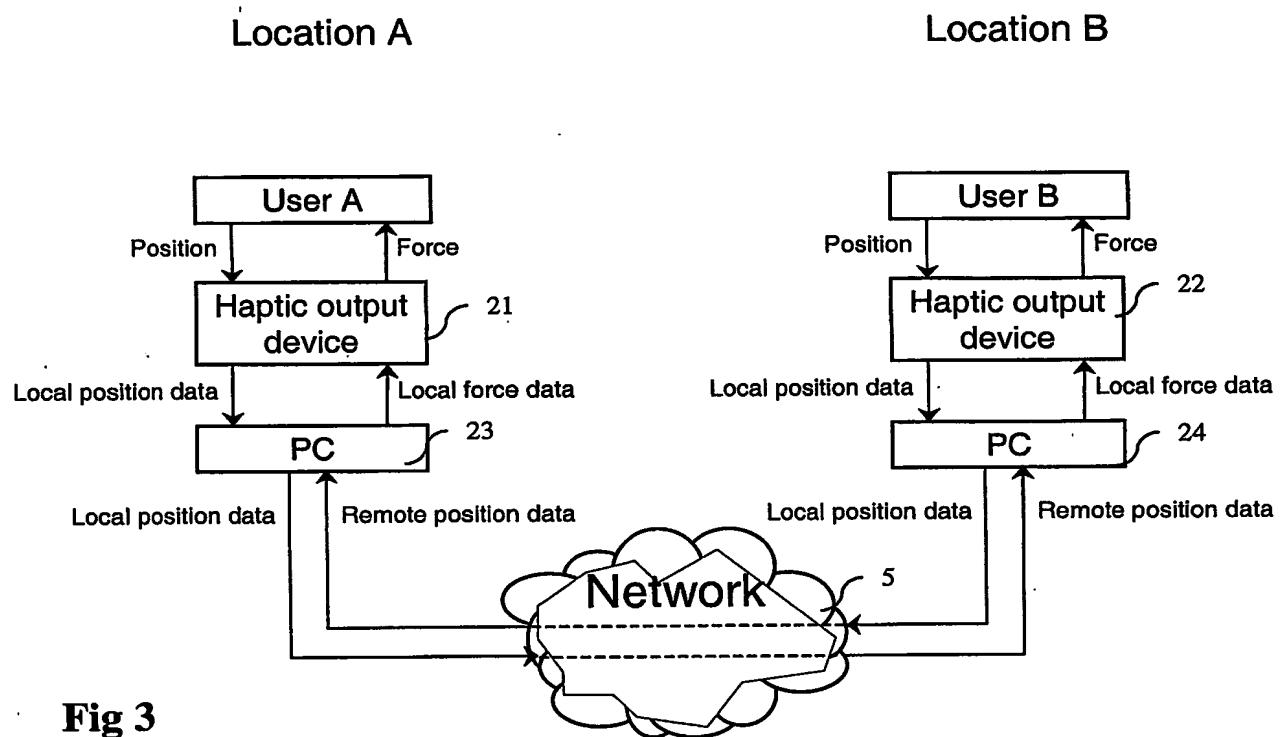


Fig 3

A Method of Latency Measurement

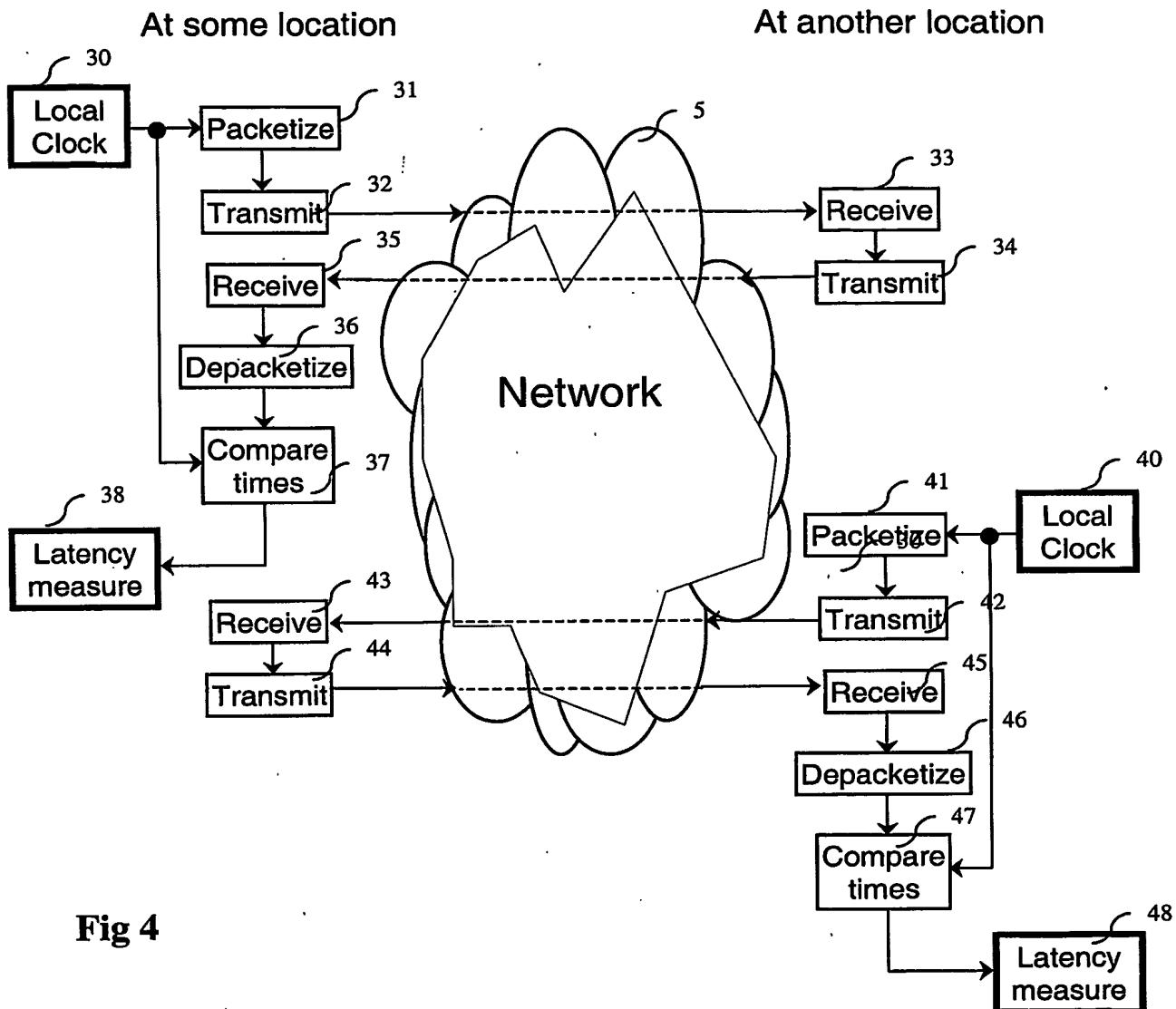
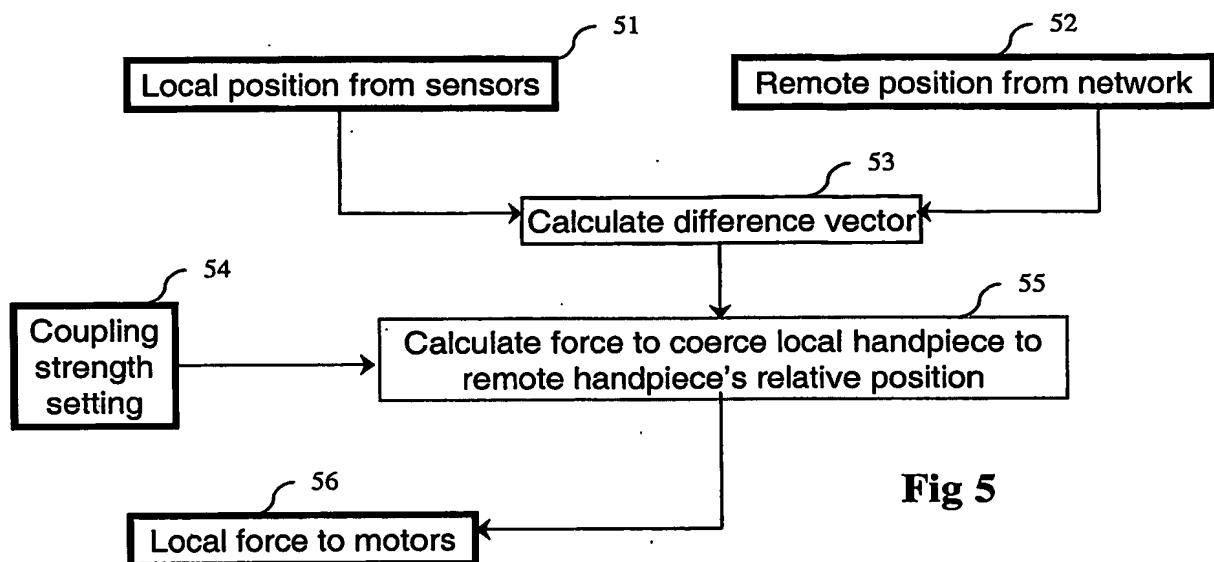
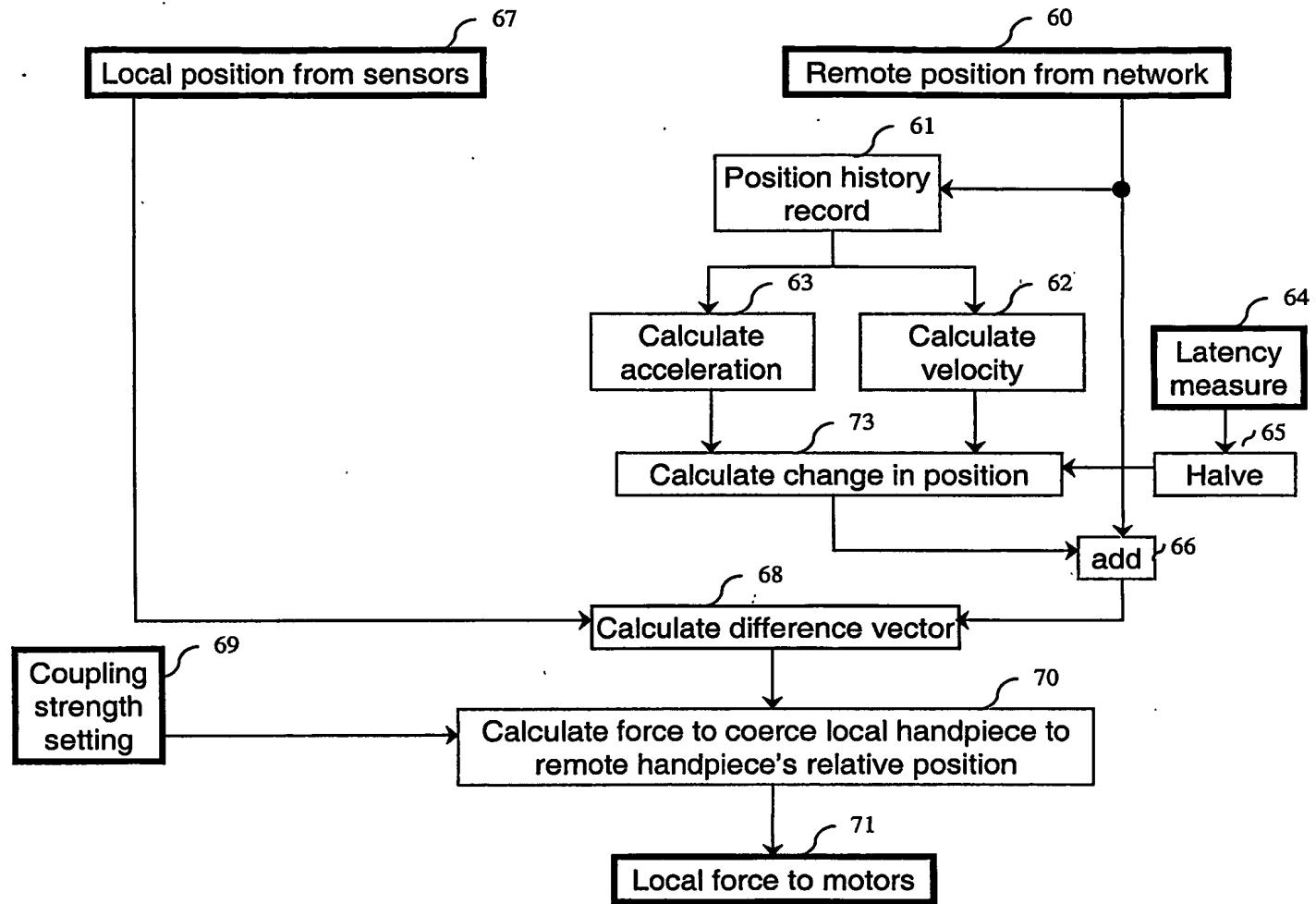


Fig 4

5/8

**Fig 5**

6/8

**Fig 6**

7/8

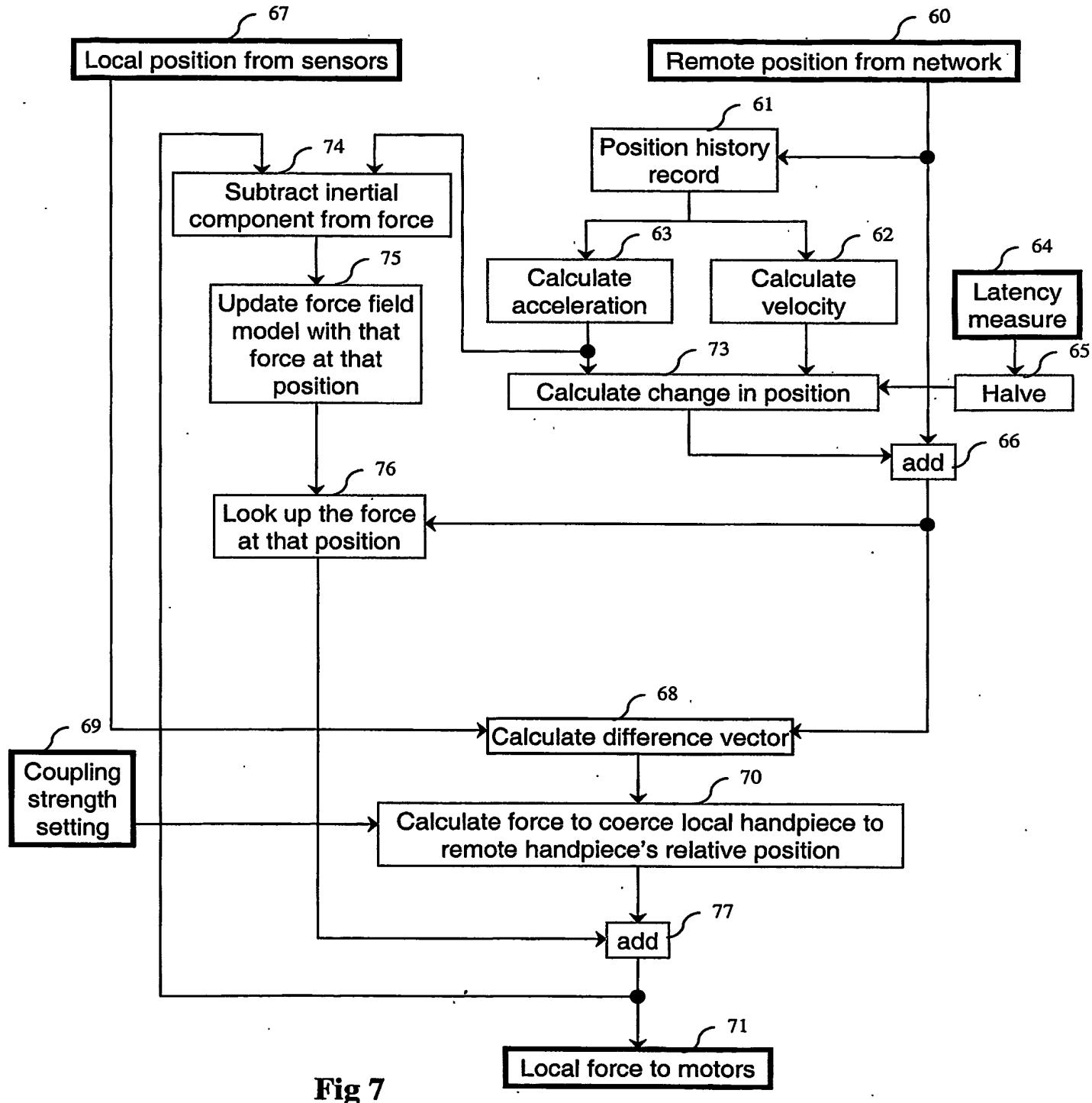


Fig 7

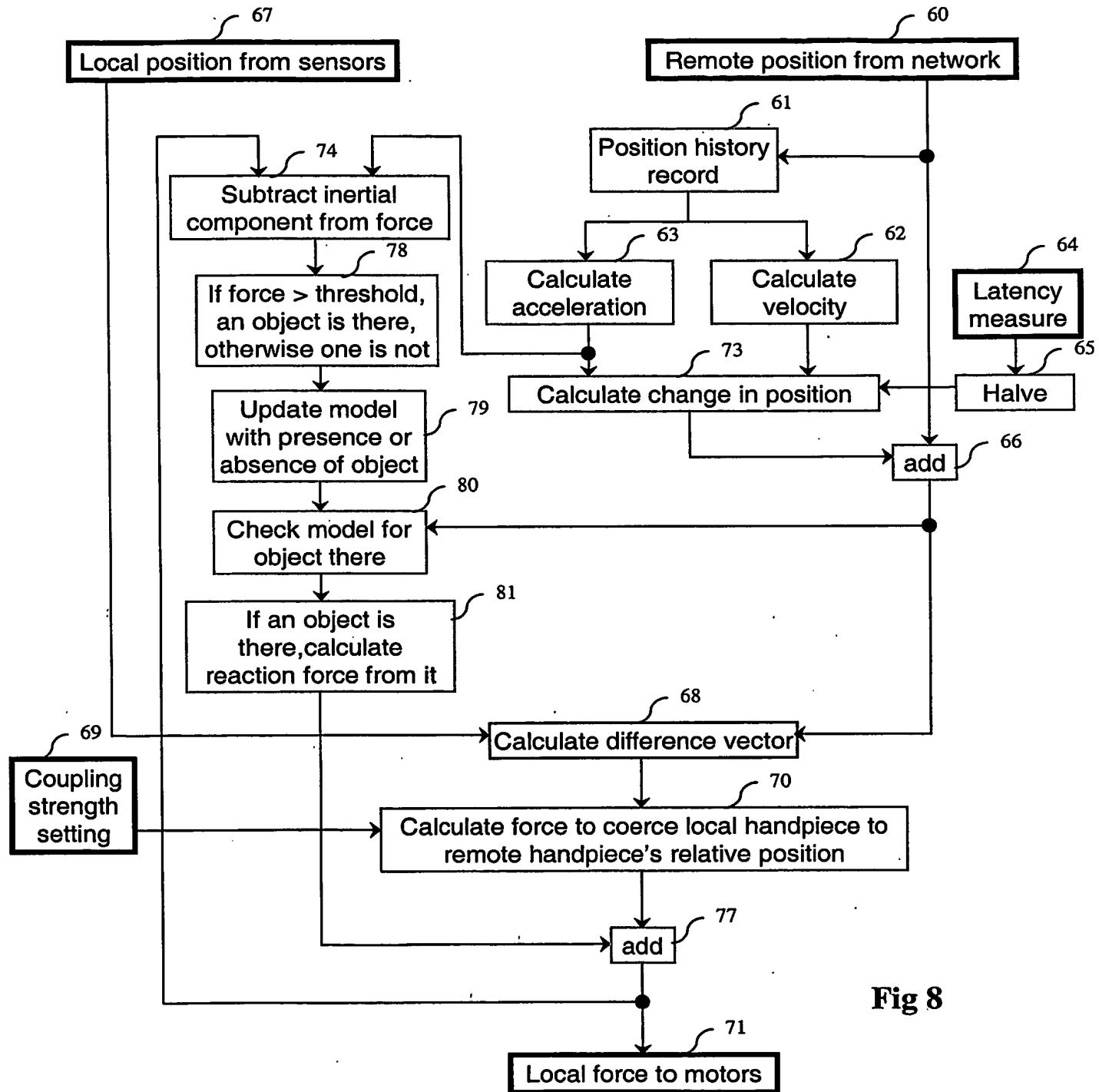


Fig 8